## Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

of 2 **Sheet** 

		Complete if Known	
	Application Number	10/039,650	
	Filing Date	December 31, 2001	
	First Named Inventor	Shiv Kaushik	
	Art Unit	2183	
_	Examiner Name	Charles Harkness	
	Attorney Docket Number	42390P13636	

	U.S. PATENT DOCUMENTS					
		Document Number	Publication Date or Issue Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
Examiner Initials*	Cite No.'	Number - Kind Code <sup>2</sup> (if known)				
<b>CH</b>	AI	US-5,530,597	06-25-1996	Bowles et al.		
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Examiner Signature(

Complete if Known **Application Number** 10/039,650 Filing Date December 31, 2001 First Named Inventor Shiv Kaushik Art Unit 2183 **Examiner Name** Charles Harkness Attorney Docket Number 42390P13636

	NON PATENT LITERATURE DOCUMENTS						
Examiner Initials*	Cite No.'	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, calalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²				
14	BI	Zilles et al., "Time-Shifted Modules: Exploiting Code Modularity for Fine Grain Parallelization, "Univ. of Wisc. Technical Report No. TR1430, October 2001, pgs. 1-21.					
CH.	82	Bradford et al., "Efficient Synchronization for Multithreaded Processors," Workshop on Multithreaded Execution, Architecture and Compilation (MTEAC), 01-31-1998, pgs. 1-4.					
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